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(54) Title: CONTROLLING RESONANT PHOTOELASTIC MODULATORS

(57) Abstract

A system and method for exploiting the dependence of a photoelastic modulator's (PEM's) resonance frequency on temperature (attributable to driving amplitude and to ambient temperature) to generally improve the performance of PEMs. In one embodiment there is provided a method and system for efficiently driving a series of multiple PEMs. To this end, each of the PEMs in a stack are separately tuned, as by controlling the power dissipated in each PEM, so that the resonance frequencies of all of the PEMs converge to a common frequency. Thus, all of the PEMs are simultaneously at resonance to ensure maximum efficiency and to maintain a selected total retardation amplitude. In another embodiment of the present invention, a single-element PEM is controlled in a manner to account for the subtle changes in the PEM's resonance frequency. To this end a control method and system is provided to keep constant both the retardation amplitude of the PEM and the actual operating frequency of the PEM, which frequency need not necessarily be the resonant frequency.

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Controlling Resonant Photoelastic Modulators

Technical Field

This application relates to a system and method for precise control of
5 resonant photoelastic modulators.

Background

A resonant photoelastic modulator (PEM) is an instrument that is used for modulating the polarization of a beam of light. A PEM employs the photoelastic effect as a principle of operation. The term "photoelastic effect" means that an 10 optical element that is mechanically strained (deformed) exhibits birefringence that is proportional to the amount of strain induced into the element.

Birefringence means that the refractive index of the element is different for different components of polarized light.

A PEM includes an optical element, such as fused silica, that has attached 15 to it a piezoelectric transducer for vibrating the optical element at a fixed frequency, within, for example, the low-frequency, ultrasound range of about 20 kHz to 100 kHz. The mass of the element is compressed and extended as a result of the vibration.

The compression and extension of the optical element imparts oscillating 20 birefringence characteristics to the optical element. The frequency of this oscillating birefringence is the resonant frequency of the optical element and is dependent on the size of the optical element, and on the velocity of the

transducer-generated longitudinal vibration or acoustic wave through the optical element.

Retardation or retardance represents the integrated effect of birefringence acting along the path of electromagnetic radiation (a light beam) traversing the 5 vibrating optical element. If the incident light beam is linearly polarized, two orthogonal components of the polarized light will exit the optical element with a phase difference, called the retardance. For a PEM, the retardation is a sinusoidal function of time. The amplitude of this phase difference is usually characterized as the retardance amplitude or retardation amplitude of the PEM.

10 In conventional PEMs, the value of the retardation amplitude is selectable by the user. Because resonant PEMs are typically driven at their resonant frequency, stress oscillations, which are induced by the transducer, can exhibit relatively large amplitudes. However, driving PEMs at their resonant frequency prevents the user from controlling the oscillation frequency.

15 Both the size and acoustic wave velocity of a PEM depend on the optical element's temperature. Consequently, the resonant frequency of a PEM will also depend on the device's temperature. In general, this temperature depends on two factors: (1) the ambient temperature, and (2) the amplitude of the stress oscillations in the optical element. At high stress amplitudes, the amount of 20 acoustic (mechanical) energy absorbed in the optical element can become significant. As the absorbed acoustic energy is converted to heat within the mass

of the element, significant temperature increases and corresponding shifts in the PEM's resonant frequency can occur.

PEMs having high retardance amplitudes are required, for example, in Fourier Transform spectral analysis (see, for instance, US Patents 4,905,169 and 5,208,651). In such applications, spectral resolution is proportional to the PEM's retardation amplitude, and useful spectral resolutions are achieved at high amplitudes, which cannot be reached by a conventional (single) PEM.

The most direct way of achieving these high-retardation amplitudes is to stack together several PEMs. In such an arrangement it is important that the sum or total of the retardation amplitudes of all of the PEMs matches the sum of the maximum retardation amplitudes of each of the PEMs in the stack.

Even if all the PEMs in a stack are driven at the same frequency, the total retardation amplitude of the stack may be less than the sum of the maximum retardation amplitudes of each of the PEMs in the stack. This is because even a relatively small spread in the resonant frequencies of the individual PEMs, which is fully consistent with manufacturing specifications, results in most of the PEMs not being driven exactly at resonance. As a result, the phases of the oscillations of the individual PEMs are not the same, even though the PEMs are driven at the same frequency, and, therefore, the total retardation amplitude is less than the sum of the individual amplitudes.

Furthermore, uneven heating of the individual PEMs when driven at high amplitudes may result in the individual resonant frequencies drifting by unequal

amounts. Consequently, the phases of the individual PEM oscillations may further diverge as the driving amplitude is increased or as the system warms up. This may lead to a further decrease in the efficiency of the PEM stack.

The problem of drifting operating frequencies is not limited to stacked PEM arrangements. In a conventional single PEM system, even though the retardation amplitude can be adjusted at will (within the limits set by the maximum driving voltage provided by the electronic circuits), the system's operating frequency is determined by the PEM's resonant frequency and, as explained above, thus depends on both ambient temperature and the amplitude at which the PEM is driven. This results in an operating frequency that drifts with ambient temperature, as well as during warm-up and after changes in the set retardation amplitude. Such a situation may be undesirable in certain applications where the PEM's operating frequency, as well as its amplitude, must be kept constant.

15

Summary of the Invention

The present invention is directed to a practical system and method for exploiting the dependence of a PEM's resonance frequency on temperature (attributable to driving amplitude and to ambient temperature) to generally improve the performance of PEMs.

20

In one embodiment of the present invention, there is provided a method and system for efficiently driving a series of multiple PEMs (such as the stacked arrangement noted above). To this end, each of the PEMs in the stack are

- separately tuned, as by controlling the power dissipated in each PEM, so that the resonant frequencies of all of the PEMs converge to a common frequency. Thus, all of the PEMs are simultaneously at resonance to ensure maximum efficiency and to maintain a selected total retardation amplitude. Thus controlled, the
- 5 common-resonance-frequency PEMs operate like a single PEM, but at a much higher retardation amplitude than that available from conventional single-element PEMs. This result makes the multiple PEM arrangement amenable to applications, such as the above-mentioned Fourier Transform spectral analysis, that require large retardation amplitudes.
- 10 In another embodiment of the present invention, a single-element PEM is controlled in a manner to account for the subtle changes in the PEM's resonant frequency. To this end, a control method and system is provided to keep constant both the retardation amplitude of the PEM and the actual operating frequency of the PEM, which frequency need not necessarily be the resonant
- 15 frequency.

Other advantages and features of the present invention will become clear upon study of the following portion of this specification and drawings.

Brief Description of Drawings

Fig. 1 is a diagram of an exemplary system for controlling multiple PEMs in
20 accordance with one aspect of the present invention.

Fig. 2 is a block diagram of a global controller and one of the local controllers employed with a preferred embodiment of the present invention.

Fig. 3 is a diagram of a preferred embodiment of a low-pass filter employed as an operator in the global controller.

Fig. 4 is a diagram of a preferred embodiment of a low-pass filter employed as an operator in the local controllers.

5 Fig. 5 is a diagram of one preferred embodiment of a circuit for generating a drive voltage waveform for a PEM.

Fig. 6 is a diagram of a system for controlling a single PEM in accordance with another aspect of the present invention.

10 Fig. 7 is a diagram of a system for controlling a single PEM in accordance with another aspect of the present invention.

Best Modes for Carrying Out the Invention

The diagram of Fig. 1 depicts an exemplary system for controlling multiple photoelastic modulators (PEMs) in accordance with one aspect of the present invention. In this embodiment, a series of three PEMs 20 are depicted. The 15 PEMs are aligned so that light from a source 22 travels in a beam 24 through each of the PEMs 20 to reach a detector 26. The optical aspects of the setup shown in Fig. 1 have been greatly simplified (polarizers, etc. being omitted) for the purposes of this description. Such a setup may be used, for example, in the Fourier Transform spectral analysis mentioned above.

20 The polarization of the light 24 is modulated by the PEMs to impart a retardation amplitude in the beam that reaches the detector. In this regard, the

optical element 28 of each PEM is vibrated by an attached piezoelectric transducer 30, thereby to introduce into the optical element (such as fused silica) the oscillating birefringence discussed above (as well as the attendant contribution to the total retardation in the light that reaches the detector).

5 In accord with one embodiment of the present invention there is provided a system for controlling the multiple PEM arrangement just described so that all of the PEMs operate at a resonant frequency and provide a total retardation amplitude (i.e., the sum of the individual values of the retardation provided by each PEM) that precisely matches a desired reference amplitude.

10 The preferred system is primarily embodied as local controllers 32, one of which is associated with each PEM 20, and a global (system) controller 34. As will be explained, a computer 36 is, optionally, provided to facilitate operation of the global and local controllers, which computer 36 may also receive the total retardation information collected by the detector 26.

15 Before discussing the particulars of the global and local controllers, it is worth pointing out that for a single PEM to be driven at resonance and with a selected retardation amplitude, one must, in general, control two parameters: (1) driving voltage and (2) driving frequency. There are also two independent error signals that can be used by a feedback loop in order to adjust these control
20 parameters. These are (1) the retardation amplitude error and (2) the phase error between PEM driving voltage and current.

The phase error relates the driving frequency to the resonant frequency of the PEM. In the simplest situation, the phase error is zero when the two frequencies are equal; that is, when the PEM is driven at resonance. (In the general case, the current-voltage phase difference corresponding to resonance driving will have a non-zero, but fixed, value. In such a case, the phase error will be the difference between the current-voltage phase difference and that fixed value.)

The retardation amplitude error is the difference between actual and selected retardation amplitudes, and vanishes when the two amplitudes are equal. The retardation amplitude error can be optically determined, or approximated by the driving current amplitude error. In the following portion of this description, the latter approximation is employed, although the former is certainly usable.

A stable feedback loop can be implemented so that the two control parameters (driving voltage and driving frequency) always change in such a way as to cancel the two error signals. When the two error signals have vanished, the PEM operates at resonance and provides the selected retardation amplitude.

Fig. 1 depicts the use of multiple (stacked) PEMs. Three PEMs are shown, although the following description will apply to groups of two or more PEMs. Thus, one can consider the series of PEMs as comprising any of a number "n" PEMs, which number is selected to suit the retardation amplitude needs of a particular application.

When "n" PEMs are stacked, there are $n+1$ control parameters (the driving voltage amplitude for each of the "n" PEMs and the common driving frequency).

It would appear that $2n$ independent error signals must be cancelled (the amplitude and phase errors for each of the "n" PEMs). This would require $2n$

5 control parameters, such as "n" individual driving voltage amplitudes and "n" individual driving frequencies. However, in accordance with the present invention, the control system requires only $n+1$ control parameters in the system.

As a result, the PEM stack is controlled (i.e., all PEMs reaching a common resonant frequency while maintaining a selected total retardation amplitude) with
10 only $n+1$ control parameters.

With reference to Fig. 2, each local controller 32 (only one of which is depicted in Fig. 2) monitors its PEM's current-voltage phase difference, as well as reference signals generated by the global controller 34, which reference signals are described more fully below. Using these signals, the local controller adjusts
15 the voltage amplitude and frequency of the PEM's driving signal.

Furthermore, each local controller 32 generates a current-voltage phase error signal that is provided to the global controller 34. The local controller also sends a local current amplitude signal to the global controller 34. Also, each local controller 32 locks the phase of its PEM current to a common phase reference,
20 preferably the phase of the common frequency signal generated by the global controller 34, as will be described.

With continued reference to Fig. 2, the exemplary one of "n" local controllers 32 drives its PEM by applying the following feedback **equation 1**:

$$V' = -D_{\tau}^{-1}(\beta\varepsilon\phi - \gamma\omega')$$

the derivation of which is provided in US Provisional Application No. 60/078,069, hereby incorporated by reference, and from which the present application claims the benefit of its filing date under 35 USC § 119e.

5 In the foregoing equation 1:

V' = the driving voltage amplitude to be applied by the local controller to its associated PEM; V' in equation 1 representing a time derivative of V ;

10 D_{τ}^{-1} = is an inverse integral operator (explained more below) for a PEM having a thermal time constant τ that is the quotient of the PEM's heat capacity and rate of heat exchange with ambient;

β = the temperature coefficient of the PEM at resonant frequency;

$\varepsilon\phi$ = the phase difference between the PEM current and the drive signal voltage applied to the PEM;

15 γ = a coefficient determined by the individual physical characteristics of the PEM; and

20 ω' = a value corresponding to the difference between a selected retardation amplitude (here approximated by a current amplitude) and the "average" retardation amplitude actually applied by the PEMs and to the sum of the phase error signals of all PEMs; this signal is the time derivative of the common driving frequency, and is supplied by the global controller.

With reference to Fig. 2, the local controller 32 employs a voltage-controlled oscillator (VCO) 40 receiving on line 42 a frequency input signal that, as described more below, sets the frequency of the constant amplitude signal that emanates from the VCO. That signal is applied to a multiplier 110 (shown as "X" in the figure and controlled as described below). The multiplier multiplies the amplitude of the VCO output signal. Thus, on line 46 there appears the drive voltage signal that is applied to drive the transducer of the PEM 20. In short, the VCO and multiplier elements can be considered as the PEM's drive waveform generator.

As noted above, the present system employs as one of two error signals the phase errors between each of the PEM drive voltage signals and the current applied to the PEM. To this end, each local controller 32 is provided with a phase comparator 50 that receives as input the drive voltage signal (via line 52) and a signal (line 54) that is proportional to the current applied to the PEM. This current signal is developed by a current pickup "I" shown at 56.

The output of the comparator 50 (line 60) comprises the phase error signal $\epsilon\Phi$ for that particular PEM. As noted above, if that error is different from zero (or, in general, from a predetermined nonzero value), then the PEM is not at resonance. As shown in Fig. 2, and with reference to equation 1, the error signal $\epsilon\Phi$ is multiplied by the temperature coefficient β (as by a multiplier 62), inverted by inverter 64 and directed to a summing circuit 66 as one of the input signals of that summing circuit. Also, the same signal $\epsilon\Phi$ is applied, via line 63, to the

global controller 34 (as are the comparable signals from the other PEMs) for processing as described below.

The summing circuit 66 receives as its other input the signal ω' . This signal, which before reaching the summing circuit is multiplied by the γ coefficient 5 at 68, is essentially representative of (1) the difference between the sum of all of the current amplitudes for the "n" PEMs and the current amplitude selected by the user to be reached by the overall system of "n" PEMs (keeping in mind that the present analysis uses the current amplitude as an approximation for the retardation amplitude) and (2) a measure of the combined phase errors $\varepsilon\Phi_i$ from 10 all of the PEMs . The value of ω' is processed by the global controller (as described below) in accordance with the following feedback equation 2:

$$\omega' = -\mu D_\tau \varepsilon I + v \varepsilon \varphi$$

the derivation of which is provided in US Provisional Application No.

60/078,069, and where:

μ = a coefficient determined by the physical characteristics of the PEMs in 15 the system;

D_τ = an integral operator, which functions as a low-pass filter;

εI = the signal representing the current error; the difference between the sum of all of the current amplitudes for the "n" PEMs and the current amplitude selected by the user to be reached by the overall system of "n" 20 PEMs; and

v = a coefficient determined by the physical characteristics of the PEMs in the system.

Before turning to a discussion of how the reference signals (ω' and a common driving frequency ω) are applied at the local controllers 32, this description will next discuss how those signals are generated by the global controller 34.

5 As to the current error ϵI , each local controller 32 provides to the global controller 34 a signal indicative of the current amplitude that is applied to that particular PEM. This is done by directing the output of the above described current pickup 56 to a root-mean-square (rms) converter 70. This converter generates a voltage signal proportional to the square root of the mean signal
10 value of the PEM current. This voltage signal, therefore, is a good measure of the PEM's current amplitude "I" and is provided on line 72 to the global controller (as are the comparable signals from the other PEMs over comparable lines).

The current amplitude signals I_i from all of the PEMs are provided to a summing circuit 74 in the global controller, the output of which is inverted by an
15 inverting amplifier 76 and applied to another summing circuit 78. That circuit has as its other input a reference signal I_{ref} that represents the current amplitude selected by the user to be reached by the overall system of "n" PEMs (hence the total retardation amplitude). The output signal $-\epsilon I$ from this summing circuit 78 represents the difference between the system current amplitude I_{ref} selected by
20 the user and the sum of all the current amplitudes for the "n" PEMs.

The signal representing ϵI passes to an operator D, which can be embodied as a low-pass filter 80 (Fig. 3) for which the following relationship exists between output and input voltages:

$$V_{\text{out}} = R/R_1 D_\tau V_{\text{in}} \text{ where } \tau = RC.$$

5 The output of the low-pass filter 80 is multiplied by the coefficient μ and thereafter provided as one of two input signals to another summing circuit 82. On another input (line 84) that summing circuit 82 receives the sum (via summing circuit 86) of all of the phase error signals $\epsilon \Phi_i$ on line 63 (and associated lines from other local controllers) from all of the "n" PEMs as multiplied by the
10 coefficient v.

The signal ω' is output from the summing circuit 82 and applied on line 90 to all of the local controllers. As noted, this signal is comprised (see equation 2, above) of two components, one corresponding to the difference between a selected retardation amplitude and the "average" retardation amplitude actually
15 applied by the PEMs and another to the sum of the phase error signals of all PEMs. As will become clear, this signal is used as a control parameter of the driving voltages of the individual PEMs.

The output of the summing circuit 82 is also applied to an integrator 92 that provides its output to a voltage controlled oscillator 96 (VCO) carried by the
20 global controller 34. The VCO 96 places on line 100, which is common to all PEMs, a single driving frequency ω .

Returning to the local controller 32, the γ -multiplied signal ω' is summed at circuit 66 with the β -multiplied and inverted phase error signal $\varepsilon\Phi$ for that particular PEM. The output (line 102) of that circuit is applied to a D^{-1} operator 104 followed by an integrator 106. These components 104, 106 may be 5 embodied as a low-pass filter 108 (see Fig. 4) where the voltage signal out is described as follows:

$$V_{out} = -1/R_i C \int D_{\tau}^{-1} V_{in}(t) dt, \text{ and with } \tau = RC.$$

This voltage output is an amplitude control signal applied, via multiplier 110, to modulate the constant amplitude output of the local controller VCO 40 so 10 that the voltage amplitude applied to the PEM (via line 46) is that for matching the overall or "average" resonant frequency of all of the system PEMs. In short, the PEMs operate in unison (at the same resonant frequency), even though the individual retardation amplitudes are neither monitored nor directly controlled.

With all of the system PEMs operating in unison, the individual mechanical 15 oscillation phases are locked to the phase of the common driving frequency signal ω . This ensures that all PEMs oscillate in phase so that the total retardation amplitude is certain to equal the sum of the individual PEM amplitudes. In this regard, the local controllers 32 employ another phase 20 comparator 120. That comparator compares the phases of the PEM current (as provided by the signal from the current pickup 56) with that of the common drive signal ω and outputs the difference (error) as the frequency input to the local

controller's VCO 40, thereby locking the local PEM to the common, system drive frequency.

Fig. 5 depicts one preferred embodiment of a waveform generator (primarily represented by components 40 and 110 of the local controller 32) for each PEM. The control voltage V from the integrator 106 is used to control the output of two high-voltage power supplies 130 and associated programmable regulators 132 of opposite polarity. The desired waveform is then obtained via a switch controller 134 that is driven by the output of the VCO to switch the output between the two power supplies. The resulting waveform has the same phase and frequency as the VCO output. The switch is implemented with high-speed, high-voltage, MOSFET transistors, which results in a simple circuit that does not introduce significant additional phase errors.

This preferred implementation uses rectangular driving waveforms. Moreover, as long as the frequency of the rectangular waveform is relatively close to the resonance frequency of the PEMs, the very narrow band pass characteristics of the latter (narrow resonance response) filter out the higher harmonics in the driving waveform. Consequently, there is no significant difference between driving the PEMs (near resonance) with rectangular and sinusoidal signals. Of course, using rectangular driving waveforms allows for simpler control of the amplitude and phase of the driving signal.

It will be appreciated that in the foregoing system the individual PEM driving voltages generated by the global and local controllers in accord with

equations 1 and 2 force the individual PEM resonant frequencies to converge to the common driving frequency. At the same time, the feedback system generates a driving frequency that tracks an "average" of the individual PEM resonant frequencies. The net result is that the common driving frequency is
5 always within the spread of resonant frequencies and that this spread tends to collapse toward that common driving frequency. Thus, although the initial individual PEM resonant frequencies may be different from each other, as well as from the driving frequency, all resonant frequencies and the driving frequency soon coalesce at a common value. From that point on, all PEMs in the stack
10 operate in unison and are driven at resonance.

Further, both the driving voltage amplitudes and the common driving frequency also depend on the total amplitude error. This dependence is such that the PEMs, while operating in unison, also track the selected total retardation amplitude. The net result is a PEM stack that operates like a single PEM, but at
15 a much higher retardation amplitude.

If the manufacturing differences between PEMs are so large that a common driving frequency that is an "average" of individual resonant frequencies cannot drive all PEMs efficiently at startup, the common driving frequency generated by the control system can be modified so as to drive most efficiently
20 the PEMs farthest in resonant frequency from the average. This can result in an initial decrease in the spread of resonant frequencies as the system warms up, to the point where the method described above can be successfully applied. Alternatively, it is also possible to drive each PEM at its particular resonant

frequency at startup, thus achieving an initial reduction in resonant frequency spread that is sufficient for the preferred control method.

Moreover, if the manufacturing differences between PEMs are large, common resonance driving may result in some PEMs running at considerably 5 higher temperatures than the others. Since the hotter PEMs are the ones being driven harder, they also contribute the most to the total retardation amplitude. In such a situation, driving levels and retardation amplitudes can be balanced by controlling the ambient temperature of the individual PEMs. The temperatures of the hotter PEMs can be maintained at elevated levels by external heating, thus 10 reducing driving amplitudes relative to those of the cooler PEMs. Ambient temperature control can be used for coarse and slow resonant frequency tuning, while the finer and faster adjustments are still made by controlling the driving voltage amplitude.

Fig. 6 illustrates a controller that may be used with a single PEM 220 so 15 that the PEM is controlled in a manner to account for subtle changes in the PEM's resonant frequency, which changes may occur as the current amplitude is increased to increase the retardation amplitude provided by the PEM. The single-PEM controller 232 keeps constant both the retardation amplitude of the PEM and the actual operating frequency of the PEM, which frequency need not 20 necessarily be the resonant frequency.

It is noteworthy that the controller 232 may be a modified version of a local controller 32 as described above, but switchable, as by control of the computer

36, into the independent or single-PEM control mode when desired by the user.

As will be explained, in this mode, the computer 36 may also be employed to supply certain reference signals to the controller 232.

In view of the dual-mode embodiment just noted, it can be appreciated that
5 many of the controller components (enclosed by dashed lines 222) can be the same as those used in the local controller embodiment 32 and, as such, carry the same reference numbers in Fig. 6. A detailed description of these components, therefore, is not repeated here.

As noted, the key to simultaneous control of the PEM amplitude and
10 operating frequency is the separate control of the phase and the amplitude of the voltage waveform used to drive the PEM. In one embodiment, the individual PEM 220 is driven by the square waveform generated as described above in connection with Fig. 5. The drive voltage signal on line 52 and the current pickup on line 54 are used as inputs to amplitude and phase feedback loops.

15 As to phase regulation, a phase-lock loop circuit includes the phase comparator 50, which receives as input the voltage signal on line 52, and the current pickup on line 54. The error signal $\epsilon\Phi$ is summed with an external phase reference signal Φ_{REF} . The output of that summing circuit 266 is applied as the frequency control input of the VCO after passing through a low-pass filter 268.
20 As a result, the driving voltage and current waveforms are phase locked, but with a phase difference determined by the external phase reference signal Φ_{REF} .

It will be appreciated that the external phase reference signal Φ_{REF} can be selected at a level that would account for driving frequency drift that occurs if the PEM's retardation amplitude is increased by an amount sufficient to change its resonant frequency. As noted, this drift is attributable, for example, to the power 5 dissipation into the PEM (with attendant temperature change) when the PEM amplitude is increased.

For a given type of PEM (such as fused silica, calcium fluoride, zinc selenide, etc.) the functional relationship between the drive amplitude and resonant frequency changes may be employed, for example as a look-up table 10 processed by the computer 36 or in a control loop (described below) to automatically establish the value of Φ_{REF} based upon the PEM's drive amplitude, thereby ensuring that a given operating frequency is maintained -even though the PEM may not be driven at its resonant frequency.

As to current regulation of the individual PEM 220, this is achieved by 15 directing the PEM current signal from the rms 70, after inverting at 270, to an input of a differential amplifier 272. The other input to the amplifier is from a user-selected reference current I_{REF} . The output of the amplifier is applied as the amplitude control of the waveform applied to the PEM.

As to frequency regulation of the individual PEM 220, reference is made to 20 Fig. 7, which defines an outer feedback loop that provides the value of Φ_{REF} (Fig. 6) for the frequency control of the VCO. Fig. 7 diagrams an analog system that phase-locks the PEM driving voltage or current to a frequency reference signal

ω_{REF} . The PEM driving voltage or current are input to a comparator 350 that provides its output, via a low-pass filter, as the control voltage that is applied as the Φ_{REF} input in the other, inner phase-lock circuit (Fig. 6). As shown in Fig. 7, a “phase control” input can be used to adjust the phase between the PEM current 5 or voltage and the frequency reference signal ω_{REF} .

In lieu of the analog circuit shown in Fig. 7, one could employ a digital outer feedback loop that has a frequency counter for receiving an A/DC-converted PEM driving voltage or current. The output of the frequency counter is compared to a set frequency by a digital processor, which correspondingly 10 adjusts the value of Φ_{REF} . Since the response time of this circuit can be relatively long, a relatively slow processor can be used. Also, the stability, accuracy, and resolution of the frequency counter only depend on how accurately and finely one wants to adjust the PEM's operating frequency.

While the present invention has been described in terms of preferred 15 embodiments, it will be appreciated by one of ordinary skill in the art that modifications may be made without departing from the teachings and spirit of the foregoing. As such, the scope of the invention is defined in the following claims and their equivalents.

Claims

1. A method of controlling a series of photoelastic modulators wherein each one of the series of "n" photoelastic modulators has a resonant frequency and is operable in response to a drive voltage and to a drive frequency, the 5 method comprising the step of tuning the photoelastic modulators during operation of the photoelastic modulators so that the resonant frequencies of all of the photoelastic modulators converge toward a common resonant frequency.
2. The method of claim 1 wherein the tuning step comprises: separately controlling the drive voltage of each photoelastic modulator; 10 and providing to each of the photoelastic modulators a common drive frequency.
3. The method of claim 1 wherein each one of the series of photoelastic modulators is controllable to impart a retardation value in electromagnetic 15 radiation that is directed through the photoelastic modulator, the method further comprising the steps of: selecting a reference retardation value; separately controlling the drive voltage of each photoelastic modulator; 20 providing to each of the photoelastic modulators a common drive frequency; and

locking the phase of oscillation of the photoelastic modulators to the common drive frequency so that the sum of the retardation values of all of the modulators matches the reference retardation value.

4. The method of claim 3 wherein the step of providing the common drive frequency is accomplished in part by generating an error signal that includes the sum of the amplitudes of current applied to drive the "n" photoelastic modulators.

5
5. The method of claim 2 wherein the step of separately controlling the drive voltage of each photoelastic modulator includes the step of adjusting the amplitude of the drive waveform in response to a reference signal that substantially reflects an average resonant frequency of the "n" photoelastic modulators.

10
6. A system for controlling a series of "n" photoelastic modulators wherein each one of the "n" photoelastic modulators has a resonant frequency and is operable in response to a drive voltage and to a drive frequency, comprising:

15
"n" local controllers, each one of the local controllers being connected with an associated one of the "n" photoelastic modulators; each local controller including:

20
a drive waveform generator responsive to a frequency input signal and to an amplitude input signal for generating a drive signal waveform for driving the photoelastic modulator at a drive voltage and a drive frequency;

- a phase comparator connected to the drive waveform generator and to the photoelastic modulator for producing a phase error signal based upon the phase difference between the current through the photoelastic modulator and the drive voltage signal applied to the photoelastic modulator; and
- 5 a current pickup circuit connected to the drive waveform generator and for producing a current amplitude signal indicative of the amplitude of the current waveform applied to the photoelastic modulator;
- 10 a global controller connected to each of the local controllers in a manner to receive the sum of the phase error signals produced by the phase comparators and the sum of the current amplitude signals produced by the current pickups, wherein the global controller includes:
- 15 control circuit means for producing a drive frequency signal for controlling the frequency input signal for all of the "n" drive waveform generators and for producing an amplitude signal for controlling the amplitude input signal for all of the "n" drive waveform generators.
7. A method for controlling a resonant photoelastic modulator that is operable in response to a drive signal, comprising the steps of:
- 20 controlling the amplitude of the drive signal; and
 controlling the frequency of the drive signal.

8. The method of claim 7 wherein the step of controlling the frequency of the drive signal includes controlling the phase difference between the voltage waveform of the drive signal and a current waveform applied to the photoelastic modulator.

5 9. The method of claim 7 wherein the step of controlling the phase difference between the drive signal voltage waveform and the current waveform applied to the photoelastic modulator includes the step of:

phase locking the drive signal voltage waveform and the current waveform.

10 10. The method of claim 9 wherein the photoelastic modulator has a resonant frequency and wherein the phase locking step includes the steps of: comparing the phase difference of the drive signal voltage waveform and the current waveform to discern an error signal based on the difference;

adjusting the error signal; and

15 applying the adjusted error signal to the drive signal, thereby to enable the photoelastic modulator to be driven at a frequency other than the resonant frequency.

11. The method of claim 7 wherein the step of controlling the frequency of the drive signal includes:

20 comparing the phase difference between a reference frequency generated external to the photoelastic modulator and the current waveform to discern an error signal; and

applying the error signal to the drive signal.

12. The method of claim 7 including the step of controlling the frequency of the drive signal separately and independently of controlling the amplitude of the drive signal.

5 13. The method of claim 7 including the step of controlling the amplitude of the drive signal with a feedback signal.

14. A controller for a resonant photoelastic modulator, comprising:
a drive waveform generator responsive to a frequency input signal and to an amplitude input signal for generating a drive signal waveform for driving the
10 photoelastic modulator at a drive current amplitude and at a drive frequency;
a phase comparator connected to the drive waveform generator and to the photoelastic modulator for producing a phase error signal based upon the phase difference between the drive signal waveform and a current waveform applied to the photoelastic modulator; and

15 a phase-lock loop circuit for receiving the phase error signal and for generating a frequency input signal based on that error signal, and for applying the frequency input signal to the waveform generator.

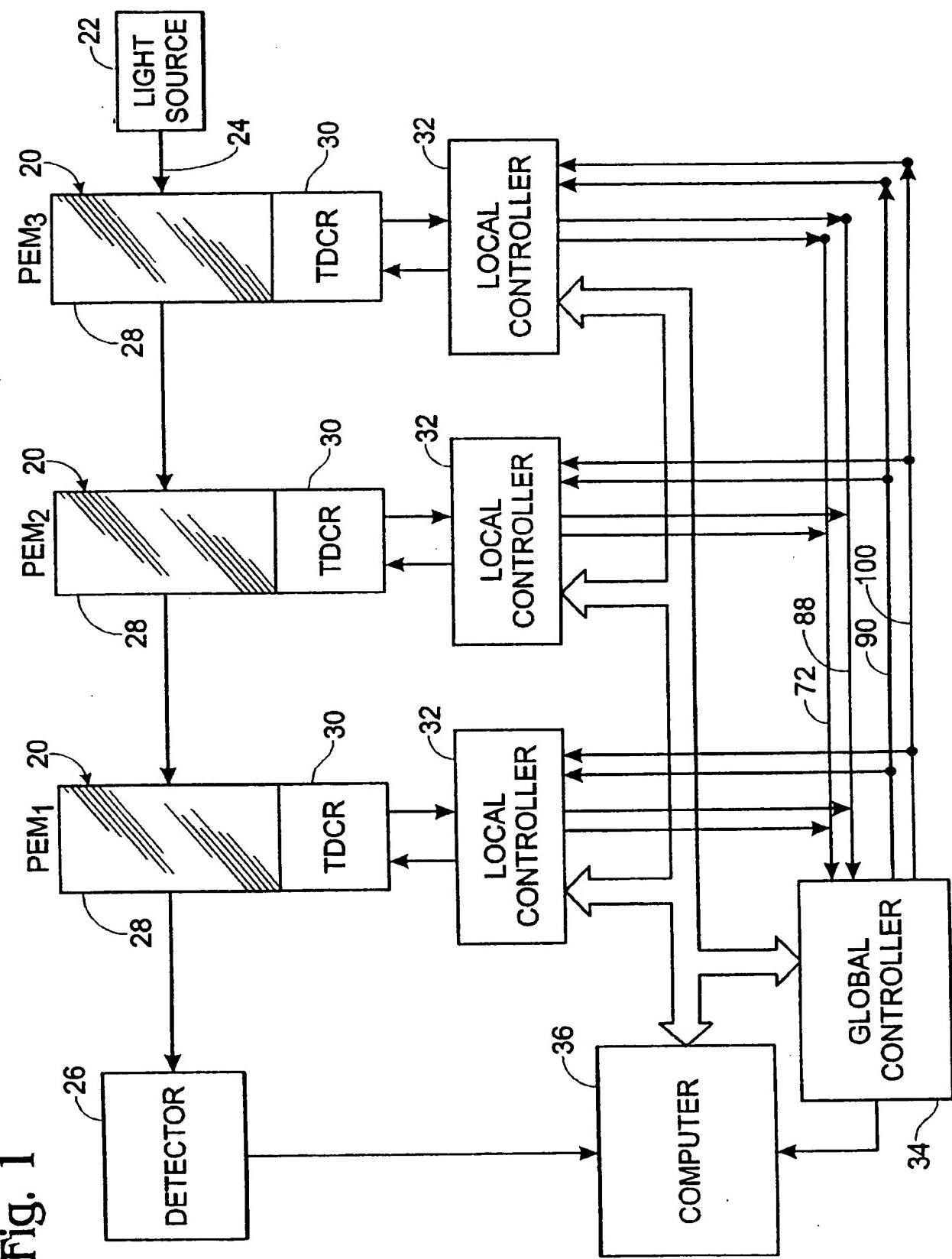
15. The controller of claim 14 wherein the photoelastic modulator has a resonant frequency that is variable with the amplitude of the drive signal that is
20 applied to it, the system further comprising:

a reference current amplitude circuit for applying to the drive waveform generator an amplitude input signal having a value representing a selected

current amplitude, and for changing the value of the amplitude input signal in response to changes in the selected current amplitude; and

tuning means for adjusting the phase error signal received by the phase-lock loop circuit in response to changes in the value of the amplitude input signal,

- 5 thereby to account for variations in the resonant frequency of the photoelastic modulator.



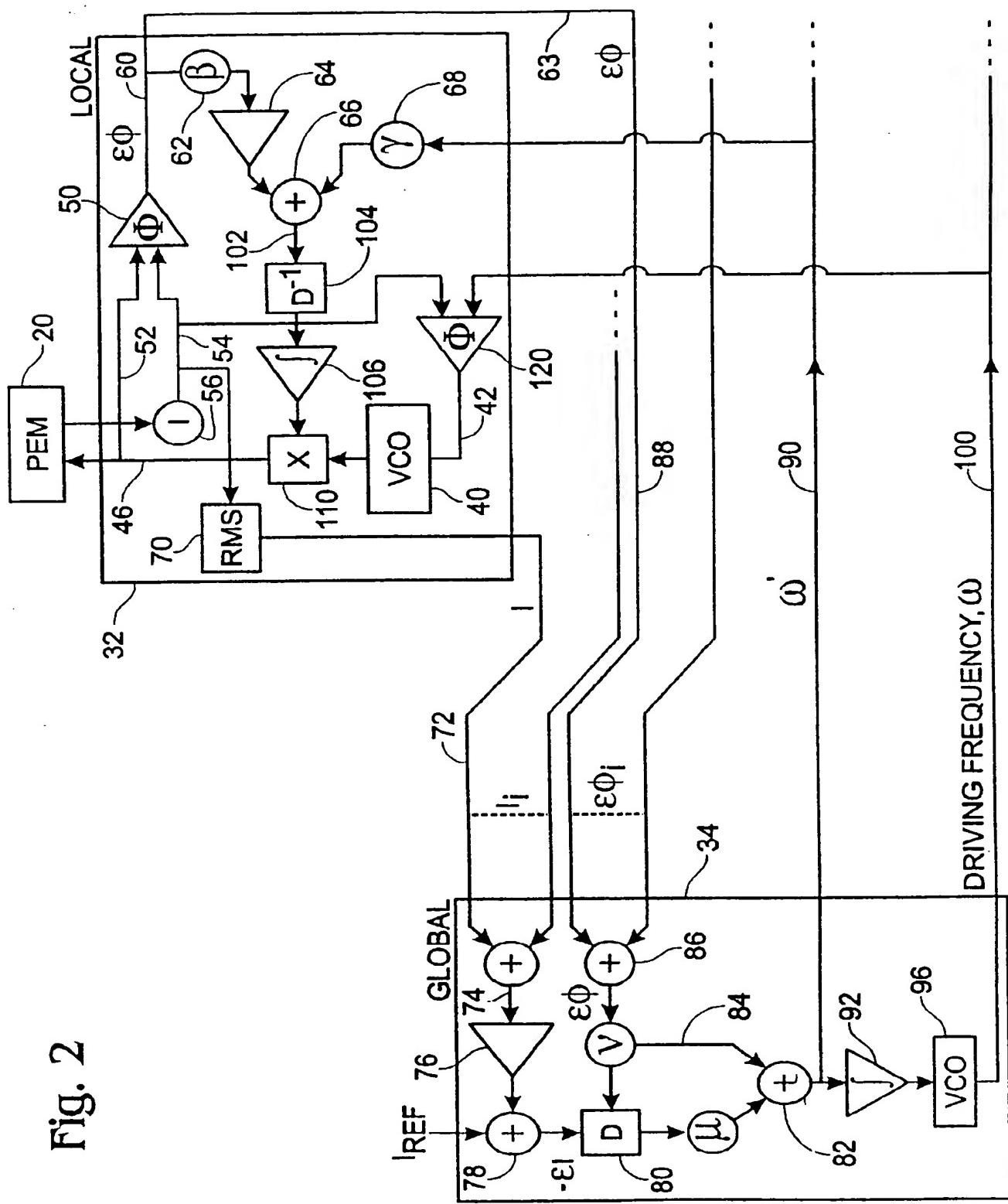


Fig. 3

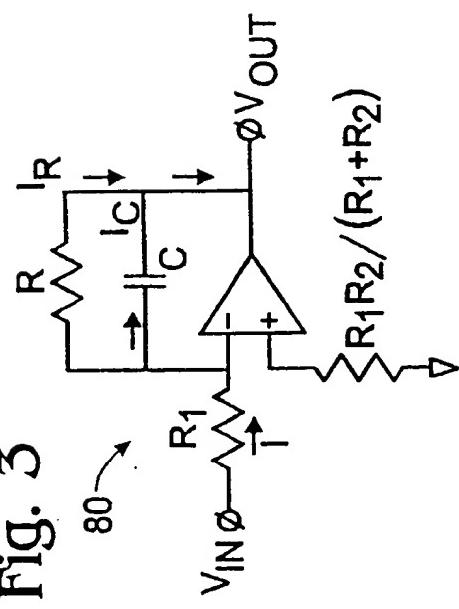


Fig. 4

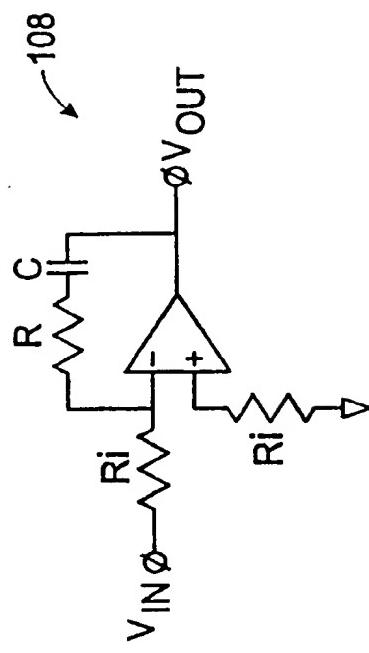


Fig. 7

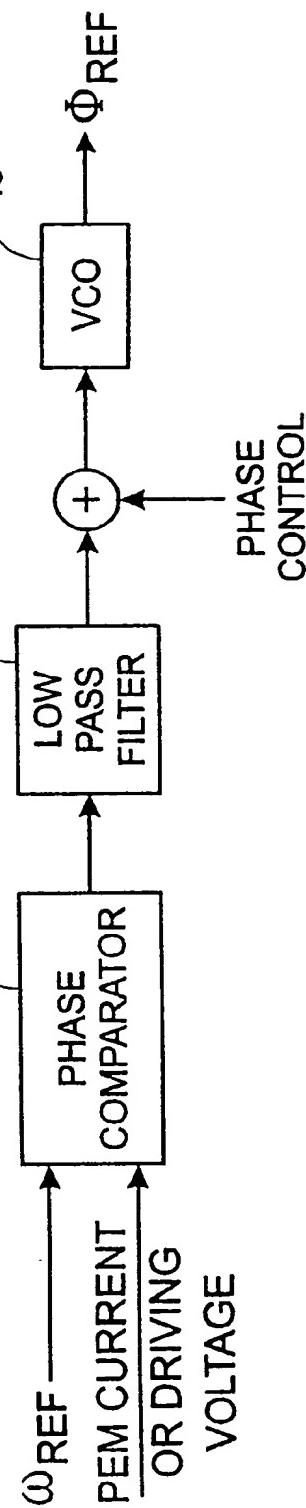
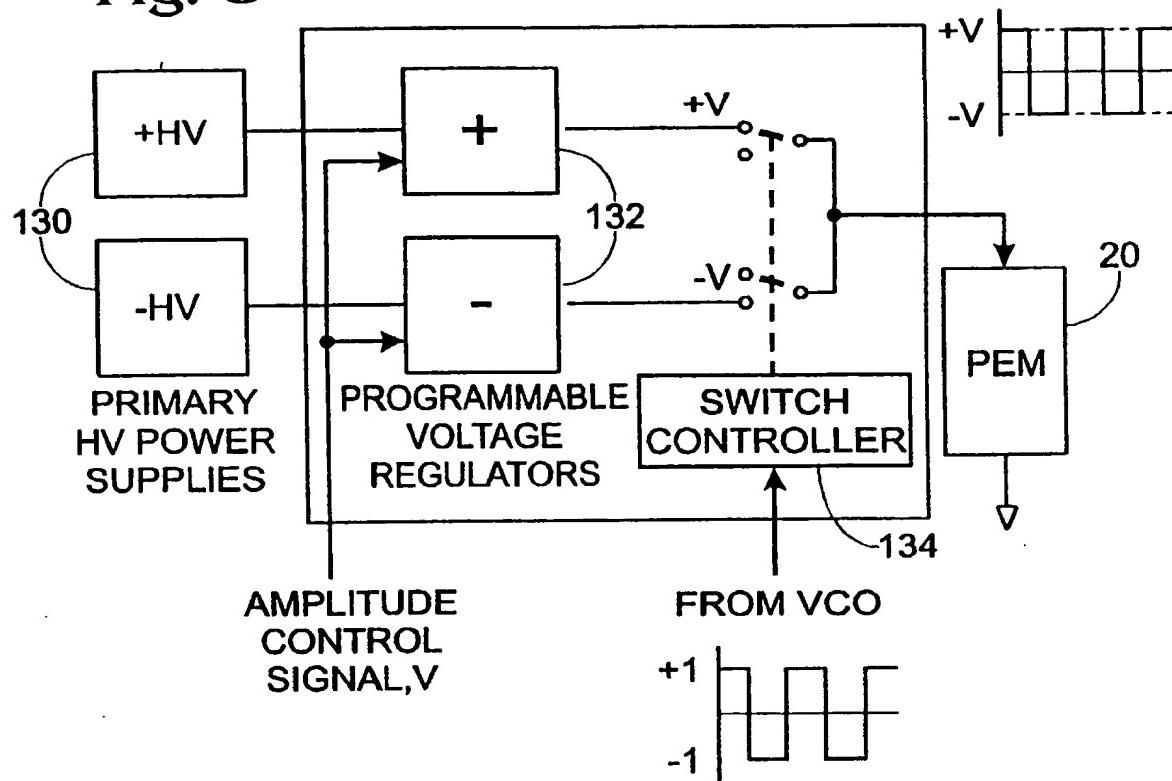
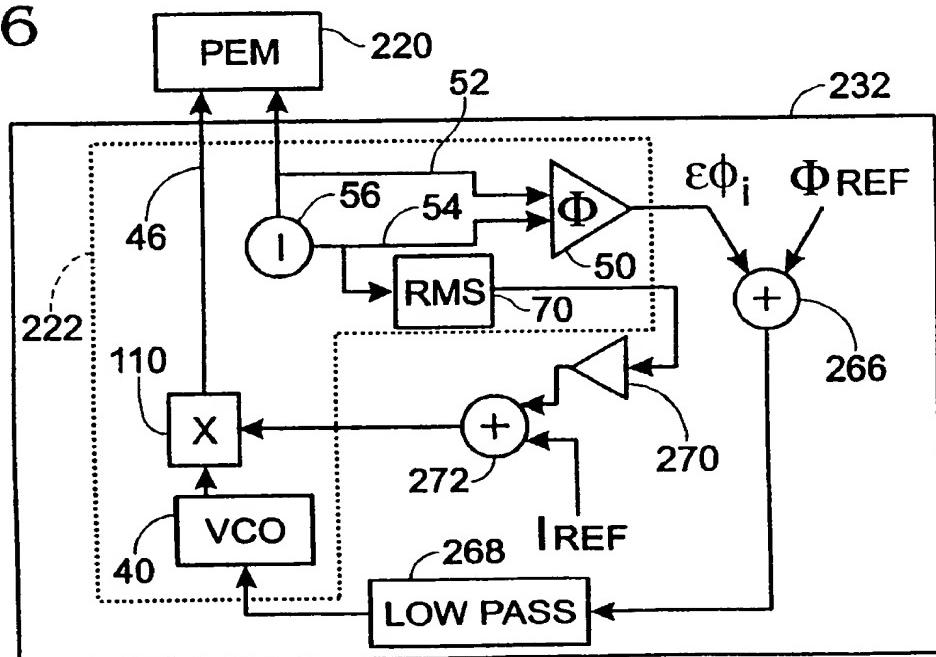


Fig. 5**Fig. 6**

INTERNATIONAL SEARCH REPORT

International application No.
PCT/US99/05586

A. CLASSIFICATION OF SUBJECT MATTER

IPC(6) : G02F 1/11, 1/33; G02B 9/02

US CL : 359/285, 308; 356/349

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

U.S. : 359/285, 308; 356/349

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

APS

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y,P	US 5,744,721 A (Varnum) 28 April 1998 (28/04/98), see entire document	1-15
A	US 5,652,673 A (Oakberg) 29 July 1997 (29/07/97), Column 1, Line 14 - Column 2, Line 22	1, 9, and 14
A	US 4,905,169 A (Buican et al) 27 February 1997 (27/02/90), Figures 1, 5, 6, 8, and 9 and claims	1, 9, and 14

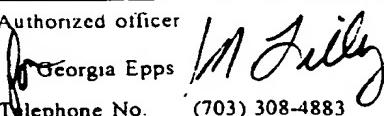
Further documents are listed in the continuation of Box C. See patent family annex.

* Special categories of cited documents	"T"	later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
A document defining the general state of the art which is not considered to be of particular relevance	"X"	document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
E earlier document published on or after the international filing date	"Y"	document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art
L document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)	"&"	document member of the same patent family
O document referring to an oral disclosure, use, exhibition or other means		
P document published prior to the international filing date but later than the priority date claimed		

Date of the actual completion of the international search	Date of mailing of the international search report
10 MAY 1999	19 MAY 1999

Name and mailing address of the ISA US
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PATENT COOPERATION TREATY

PCT

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130 26 APR 2000

INTERNATIONAL PRELIMINARY EXAMINATION REPORT

(PCT Article 36 and Rule 70)

Applicant's or agent's file reference 1074-016	FOR FURTHER ACTION See Notification of Transmittal of International Preliminary Examination Report (Form PCT/IPEA/416)	
International application No. PCT/US99/05586	International filing date (day/month/year) 16 MARCH 1999	Priority date (day/month/year) 16 MARCH 1998
International Patent Classification (IPC) or national classification and IPC IPC(7): G02F 1/11, 1/33; G02B 9/02 and US Cl.: 359/285, 308; 356/349		
Applicant HINDS INSTRUMENTS, INC		

1. This international preliminary examination report has been prepared by this International Preliminary Examining Authority and is transmitted to the applicant according to Article 36.
2. This REPORT consists of a total of 3 sheets.

This report is also accompanied by ANNEXES, i.e., sheets of the description, claims and/or drawings which have been amended and are the basis for this report and/or sheets containing rectifications made before this Authority. (see Rule 70.16 and Section 607 of the Administrative Instructions under the PCT).
 These annexes consist of a total of 0 sheets.
3. This report contains indications relating to the following items:
 - I Basis of the report
 - II Priority
 - III Non-establishment of report with regard to novelty, inventive step or industrial applicability
 - IV Lack of unity of invention
 - V Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement
 - VI Certain documents cited
 - VII Certain defects in the international application
 - VIII Certain observations on the international application

Date of submission of the demand 14 OCTOBER 1999	Date of completion of this report 06 JANUARY 2000
Name and mailing address of the IPEA/US Commissioner of Patents and Trademarks Box PCT Washington, D.C. 20231	Authorized officer GEORGIA EPPS Telephone No. (703) 308-4883
Facsimile No. (703) 305-3230	

INTERNATIONAL PRELIMINARY EXAMINATION REPORT

International application No.

PCT/US99/05586

I. Basis of the report

1. With regard to the elements of the international application:*

 the international application as originally filed the description:

pages 1-21 _____, as originally filed
 pages NONE _____, filed with the demand
 pages NONE _____, filed with the letter of _____

 the claims:

pages 22-27 _____, as originally filed
 pages NONE _____, as amended (together with any statement) under Article 19
 pages NONE _____, filed with the demand
 pages NONE _____, filed with the letter of _____

 the drawings:

pages 1-4 _____, as originally filed
 pages NONE _____, filed with the demand
 pages NONE _____, filed with the letter of _____

 the sequence listing part of the description:

pages NONE _____, as originally filed
 pages NONE _____, filed with the demand
 pages NONE _____, filed with the letter of _____

2. With regard to the language, all the elements marked above were available or furnished to this Authority in the language in which the international application was filed, unless otherwise indicated under this item.
 These elements were available or furnished to this Authority in the following language _____ which is: the language of a translation furnished for the purposes of international search (under Rule 23.1(b)). the language of publication of the international application (under Rule 48.3(b)). the language of the translation furnished for the purposes of international preliminary examination (under Rules 55.2 and/or 55.3).

3. With regard to any nucleotide and/or amino acid sequence disclosed in the international application, the international preliminary examination was carried out on the basis of the sequence listing:

 contained in the international application in printed form. filed together with the international application in computer readable form. furnished subsequently to this Authority in written form. furnished subsequently to this Authority in computer readable form. The statement that the subsequently furnished written sequence listing does not go beyond the disclosure in the international application as filed has been furnished. The statement that the information recorded in computer readable form is identical to the written sequence listing has been furnished.4. The amendments have resulted in the cancellation of: the description, pages NONE the claims, Nos. NONE the drawings, sheets/fig NONE5. This report has been drawn as if (some of) the amendments had not been made, since they have been considered to go beyond the disclosure as filed, as indicated in the Supplemental Box (Rule 70.2(c)).**

* Replacement sheets which have been furnished to the receiving Office in response to an invitation under Article 14 are referred to in this report as "originally filed" and are not annexed to this report since they do not contain amendments (Rules 70.16 and 70.17).

**Any replacement sheet containing such amendments must be referred to under item 1 and annexed to this report.

INTERNATIONAL PRELIMINARY EXAMINATION REPORT

International application No.

PCT/US99/05586

V. Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement**1. statement**

Novelty (N)	Claims <u>1-15</u>	YES
	Claims <u>NONE</u>	NO
Inventive Step (IS)	Claims <u>1-15</u>	YES
	Claims <u>NONE</u>	NO
Industrial Applicability (IA)	Claims <u>1-15</u>	YES
	Claims <u>NONE</u>	NO

2. citations and explanations (Rule 70.7)

Claims 1-15 meet the criteria set out in PCT Article 33(2)-(4), because the prior art does not teach or fairly suggest the combination and arrangement of features of the claims which include a method of controlling a series of photoelastic modulators and a controller for a resonant photoelastic modulator with a drive waveform generator, a phase comparator, a phase-lock loop circuit, a reference current amplitude circuit, and tuning means, and also, the controller and method can be used in industry.

----- NEW CITATIONS -----

NONE